GAU 2822

TRANSMITTAL LETTER General - Patent Pending)	RECE!"JA	Docket No. EN9-98-141
In Re Application Of: Sebesta etcal. AUG 3 1 2001		
Serial No. Filing Date 09/526,957 3/16/00	Examiner Mitchell, J.	Group Art Unit 2822
Title: VARIABLE THICKNESS PADS ON A SUBSTRATE SURFACE		
TO THE ASSISTANT COM Transmitted herewith is: Amendment	MISSIONER FOR PATENTS:	
in the above identified application. ☑ No additional fee is required. ☐ A check in the amount of is attached. ☑ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 09-0457(IBM) as described below. A duplicate copy of this sheet is enclosed. ☐ Charge the amount of ☑ Credit any overpayment. ☑ Charge any additional fee required.		
Jack P. Friedman Reg. No. 44,688 Schmeiser, Olsen & Watts 3 Lear Jet Lane, Suite 201 Latham, NY 12110 (518) 220-1850	on 8/29/01 first class mail unde Assistant Commiss 20231.	document and fee is being deposited with the U.S. Postal Service as a 37 C.F.R. 1.8 and is addressed to the sioner for Patents, Washington, D.C. Ferson Mailing Correspondence Kim Dwileski

Typed or Printed Name of Person Mailing Correspondence



DOCKET NO. EN9-98-141

9.60 3.19.02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sebesta et al.

Examiner: Mitchell, J.

Serial No.: 09/526,957

Art Unit: 2822

Filed: 3/16/00

For: VARIABLE THICKNESS PADS ON A SUBSTRATE SURFACE

Commissioner for Patents Washington D.C. 20231

Sir:

This paper is being filed in response to the Final Office Action mailed July 5, 2001.

Applicants respectfully request that the above-identified application be reconsidered in view of the Amendments and Remarks that follow, that each of the presently pending claims be allowed, and that the application be passed to issue.

Amendment

Please amend the above-referenced patent application as follows:

In The Claims

The following claims 1-13 are currently pending.

1. (AMENDED) An electronic structure, comprising:

a substrate;

a first circuit line including a first conductive pad and having a first thickness, wherein the first circuit line is coupled to the substrate and is not embedded into the substrate; and

350